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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Original Patent

Patentees: Alex Kalnitsky
Yih-Shung Lin

Patent No.: 5,986,330

Title: ENHANCED PLANARIZATION
TECHNIQUE FOR AN
INTEGRATED CIRCUIT

Issued: November 16, 1999

Atty Dk No.: 93-C-023C3

Reissue Application

Applicants: Alex Kalnitsky
Yih-Shung Lin

Serial No.: 09/998,595

Title: ENHANCED PLANARIZATION
TECHNIQUE FOR AN
INTEGRATED CIRCUIT

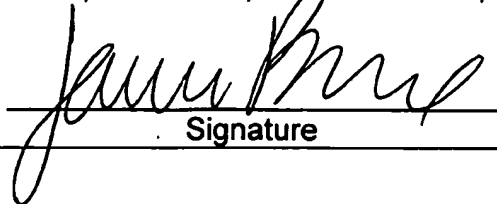
Filing Date: November 16, 2001

Atty Dk No.: 93-C-023RE (1678-42)

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AMENDMENT AND RESPONSE

Please amend the above-referenced patent application as follows.